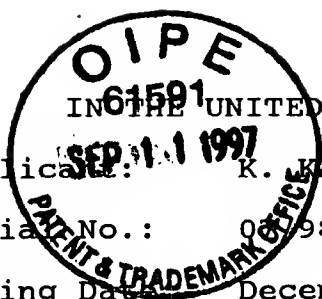


#24/F

10-24-97

MJC



UNITED STATES PATENT AND TRADEMARK OFFICE

500.26967R00

Applicant: K. KATSURA, et al.

Serial No.: 08/985,141

Filing Date: December 3, 1992

For: GRAPHIC PROCESSING APPARATUS  
UTILIZING IMPROVED DATA TRANSFER  
TO REDUCE MEMORY SIZE

Group: 2412

Examiner: M. Zimmerman

AMENDMENT

Assistant Commissioner for  
Patents  
Washington, DC 20231

September 11, 1997

Sir:

The following amendments and remarks are being  
submitted in response to the Office Action dated March 11,  
1997.

IN THE CLAIMS:

Please amend the claims as follows:

Sub G1> 44. (three times amended) A graphic processing  
apparatus comprising:

a memory for storing graphic data;

a data processor for executing a predetermined  
graphic processing to generate graphic data to be stored in  
said memory;

output means for outputting said graphic data read  
out from said memory;

a memory controller for controlling data transfer  
between said memory and said data processor in accordance

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